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MAY 8 2001  
PATENT & TRADEMARK OFFICE

\$IN 08/650,719

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Jeffrey S. Mailloux et al. Examiner: Hong Kim  
Serial No.: 08/650,719 Group Art Unit: 2185  
Filed: May 20, 1996 Docket: 303.623US1  
Title: ASYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE  
SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION

1027/11  
Amndt  
5-9-01  
OC

**AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.111**

Commissioner for Patents  
Washington, D.C. 20231

Applicant has reviewed the Office Action mailed on January 31, 2001. Detailed comments to the Office Action are found in the Remarks section below. Please amend the above-identified patent application as follows:

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**IN THE CLAIMS**

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. Specific amendments to individual claims are detailed in the following marked up set of claims.

Please amend the claims as follows:

61. (Twice Amended) A method for accessing several different locations in an asynchronously-accessible memory device, comprising:

selecting a pipeline mode of operation;

providing a new external address for every access associated with asynchronously accessing the asynchronously-accessible memory device while in [the] a burst mode of operation;

switching modes to [a] the burst mode of operation; [and]

providing an initial external address associated with asynchronously accessing the asynchronously-accessible memory device in the pipelined mode of operation; and

while in the burst mode of operation, generating at least one subsequent internal address patterned after the initial external address provided while in the pipelined mode of operation.